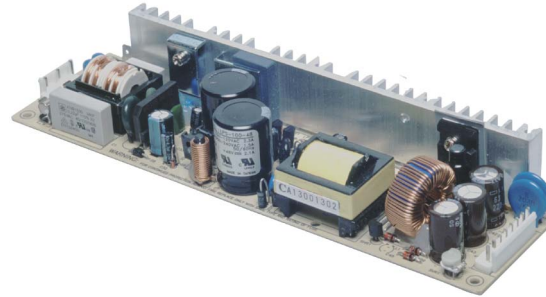


### Features

- Low cost, high reliability
- 105°C output capacitor
- Universal AC input range
- High efficiency, low temperature rise
- Soft-start circuit, limiting AC surge current
- Short circuit, overload, over-voltage protected
- Compact size, light weight
- 100% full load burn-in test
- Built in EMI filter, low ripple noise
- Built in PFC circuit
- Power factor: 0.98 typ.

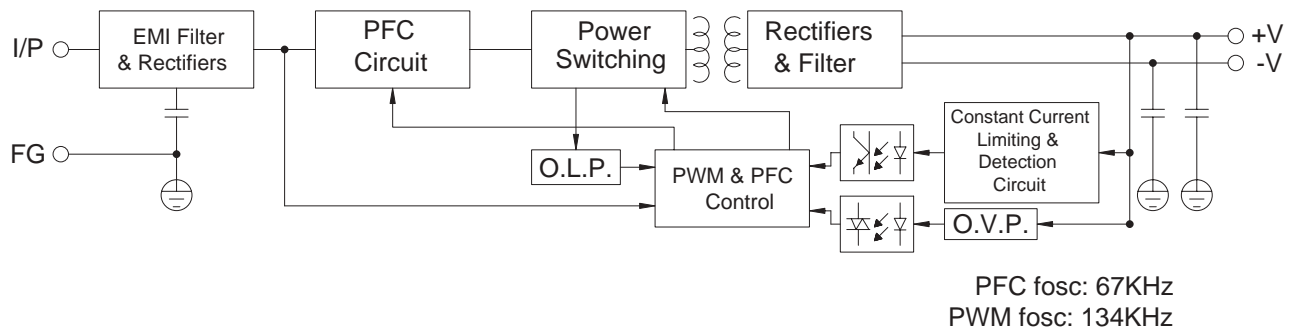


<b>DC output voltage</b>	3.3V
<b>Output V. tolerance</b>	±2%
<b>Output rated current</b>	20A
<b>Output current range</b>	0-20A
<b>Ripple &amp; noise</b>	100mVp-p
<b>Line regulation</b>	±0.5%
<b>Load regulation</b>	±1%
<b>DC output power</b>	66W
<b>Efficiency</b>	69%
<b>DC voltage adj.</b>	+10, -5%
<b>Input voltage range</b>	85~264VAC, 47~63Hz; 120~370VDC
<b>Inrush current</b>	cold start 40A//230V
<b>Leakage current</b>	<2mA/240VAC
<b>Overload protection</b>	105%~150% type: continuous current limiting, auto recovery
<b>Over voltage protection</b>	115%~135% type: shutdown, RE power on
<b>Over temp. protection</b>	---
<b>Temperature coefficient</b>	±0.05% / °C (0~50°C)
<b>Set up, rise, hold up time</b>	600ms, 30ms, 20ms/230VAC; 1.2s, 30ms, 20ms/115VAC
<b>Power factor</b>	PF>0.95/230VAC, PF>0.98/115VAC
<b>Vibration</b>	10~500Hz, 2G 10min./1cycle (1 hour each axes)
<b>Withstand voltage</b>	I/P-O/P: 3KVAC, I/P-FG: 1.5KVAC, O/P-FG: 0.5KVAC, for 1 min.
<b>Isolation resistance</b>	I/P-O/P, I/P-FG, O/P-FG:500VDC / 100M Ohms min.
<b>Working temp., humidity</b>	-10°C~+60% (refer to output derating curve), 20%-90% RH
<b>Storage temp., humidity</b>	-20°C~+85°C, 10%~95% RH
<b>Dimensions</b>	8.74x2.44x1.26 inches (222x62x32mm)
<b>Weight</b>	1.12lbs (0.51Kgs)
<b>Safety standards</b>	UL60950, TUV EN60950
<b>EMC standards</b>	CISPR22 (EN55022) CLASS B, EN61000-3-2,3; EN61000-4-2,3,4,5,6,8,11; ENV50204

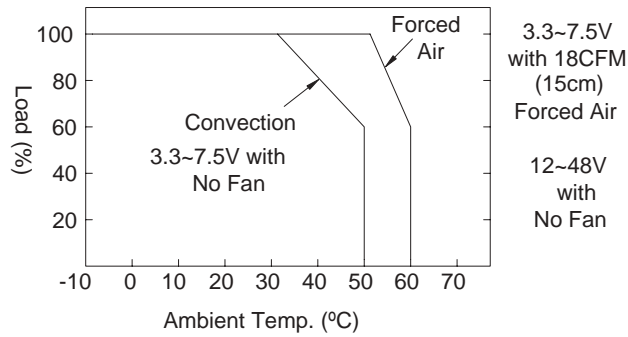
- Notes:
1. All parameters are specified at 230VAC input, rated load, 25°C 70% RH ambient
  2. Tolerance includes set up tolerance, line regulation, load regulation
  3. Ripple & noise are measured at 20MHz by using a 12" twisted pair terminated with a 0.1uF & 47 uF capacitor
  4. Line regulation is measured from low line to high line at rated load
  5. Load regulation is measure from 0% to 100% rated load

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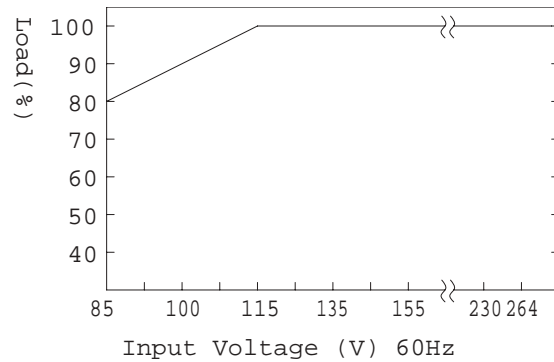
### Block Diagram



### Output Derating



### Static Characteristics



### Dimensions (mm)

#### Terminal Pin No. Assignment

CN1: JST B5P-VH  
Pin 1,3,5: AC Input  
CN2: JST B8P-VH  
Pin: 1,2,3,4: DC Output -V  
Pin: 5,6,7,8: DC Output +V

#### Mating Connectors

CN1,2 Mating connector type  
JST VHR Series or equivalent with  
JST SVH-21T-1.1 or equivalent  
crimp terminals.

